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Gravity Probe B Relativity Mission

BOARD-LEVEL TUNING PROCEDURE FOR THE GYROSCOPE SUSPENSION SYSTEM (GSS) MUX/OSCILLATOR/CHARGE CTRL (MUX) BOARD

PWA 8A01883 Rev C S/N:

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GP-B Procedure P0611 Rev A

September 08, 2000

Prepared by: William Bencze Date
PWA Responsible Engineer

Approved by: William Bencze Date
RE, Gyroscope Suspension System (GSS) Group

Approved by: Dorrene Ross
GP-B Quality Assurance

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1.0 Revision History

Rev Level	Comments/notes	Date	Revised By
-	First release of this test procedure	1-May-00	B. Bencze
A	Modifications based on dry run on GSS gold system cards	7-Sep-00	B. Bencze

2.0 Scope:

This procedure details the board-level electrical functional tests on the GSS MUX/Oscillator/Charge Control (MUX) card. No mechanical or thermal stress testing shall be performed at this time.

This test plan has been written to be run with the GSS "Gold System" test fixture – an electrically and interface equivalent of the GSS flight units. In General, the Device Under Test (DUT) shall be inserted into the Gold System in place of the equivalent Gold System card, any additional electrical connections to the Gold System shall be made, and a set of software-based and possibly manual tests will be run on the board. Upon successful completion of this procedure, this board is considered electrically functional.

All data recorded during this test is recorded in this document; each test of a board will use its own copy of this procedure, and will be identified by serial number on the cover sheet.

3.0 Reference Documents

- 3.1. GSS Gold System Hardware and Software Configuration Standard, P0663
- 3.2. PWA Drawing, GSS MUX board, 8A01883
- 3.3. PWB Drawing, GSS MUX board, 8A01889
- 3.4. PWA schematic, PC610A gold system test card.
- 3.5. PWA schematic, PC620A gold system test card.
- 3.6. Electrostatic Discharge Control Program for Protection of Electrical and Electronic Parts, Assemblies, and Equipment, MIL-STD-1686

4.0 Test Facilities

HEPL Room 127, Stanford University

5.0 QA Provisions:

- 5.1. This procedure shall be conducted on a formal basis to its latest approved and released version. The QA Program Engineer (D. Ross) and the ONR representative (E. Ingraham) shall be notified 24 hours prior to the start of this procedure. QA may monitor the execution of all or part of this procedure should they elect to do so.

Date/time: _____
GP-B QA (D. Ross)

Date/time: _____
ONR (E. Ingraham)

- 5.2. Upon completion of this procedure, the GSS manager and the GP-B QA manager shall certify her/his concurrence that the procedure was performed and accomplished in accordance with the prescribed instructions by signing and dating his approval at the end of this procedure.

6.0 Test Personnel

This test procedure is to be conducted only by the following personnel:

- 6.1. William Bencze
- 6.2. Lo Van Ho

7.0 General Instructions

- 7.1. Redlines can be initiated by the test personnel listed in Section 6.0 and must be approved by QA.
- 7.2. Test operators shall read this procedure in its entirety and resolve any apparent ambiguities prior to beginning this test.
- 7.3. Any nonconformance or test anomaly should be reported by a Discrepancy Report. Refer to the Quality Plan, P0108, for guidance. Do not alter or break test configuration if a test failure occurs; notify quality assurance.
- 7.4. Only the following persons have the authority to exit/terminate this test or perform a retest: Test operators listed in Section 6.0 and GP-B QA.
- 7.5. In this document, "Perform Flight S/W system test commands:" means to prepare the test system software as described in P0670 Board-Level Test Software Operational Procedure, and then issue the listed commands according to the procedure described in P0670.

8.0 Hardware Safety Requirements:

- 8.1. This assembly is ESD sensitive; special care shall be exercised per the "Electrostatic Discharge Control Program for Protection of Electrical and Electronic Parts, Assemblies, and Equipment", MIL-STD-1686
- 8.2. Ensure that power is removed from cable assemblies before connecting or disconnecting cable connectors.
- 8.3. Examine all mating connectors before attempting to mate them. Remove any foreign particles. Look for any damaged pins or sockets. Do not force the coupling action if excessive resistance is encountered. Ensure that key-ways are aligned when mating connectors.

9.0 Equipment Pretest Requirements:

- 9.1. The GSS Gold System in which this board is to be tested must have passed successfully the P0663 – Gold System Certification Procedure prior to the start of this test. Record the Gold System serial number and date of its certification, below

GSS Gold System	SN:	
	Date of Certification	
	Configuration (circle one)	Full Partial

10.0 Additional Test Equipment

The following support hardware, test equipment, or software will be used and the applicable information for the instruments shall be recorded below. Hand-written additions to this list may be made in the space provided.

Equipment Description	Make	Model	SN	Cal Due
1. Multimeter	Fluke			
2. Multimeter	Fluke			
3. Multimeter	Fluke			
4. Gain/Phase meter	HP			
5. BNC-to-Pomona mini-clip test cable, 3'	SU fab	NA	NA	NA
6. Signal Generator	SRS			
7. Oscilloscope	Tek			
8. 6 ea 100 kohm tuning pot.	SU	NA	NA	NA
9. 1 ea 200 kohm tuning pot.	SU	NA	NA	NA
10. PC620 gold system test card	SU	Rev A		NA
11. PC610 gold system test card	SU	Rev A		NA
12. Bolt-up FSU enclosure	LMMS	-		NA
13. Gold system Fwd backplane with banana-plug power port	SU PC400		002-GS	NA
14. Triple output power supply	HP			
15. BNC patch cables, as required	Various	NA	NA	NA
16. Meter test leads, as required	Various	NA	NA	NA

11.0 Device Under Test (DUT):

Record the serial number of the Device Under Test, or DUT.

PWA 8A01883 GSS MUX Card	SN:	
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Test Operator:	Name:	
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Start of test:	Date:	
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12.0 Pre-test visual inspection.

Note: All handling of this PWA shall be performed using ESD control methods, as outlined in MIL-STD-1686. Unit shall be inspected at an ESD certified station. Wrist straps and/or heel grounding straps shall be used.

P/F	Test/Activity	Notes
	12.1. Remove PWA from storage container.	
	12.2. Verify that the PWA board revision is C. <i>(Component changes are required to the Rev B design before tuning can begin; see PWA traveller for rework instructions)</i>	
	12.3. Verify that no parts are missing, unless called out in the assembly drawing.	
	12.4. Verify that the following capacitors are installed in the proper orientation: C49, C50, C51	
	12.5. Verify the proper orientation of pin 1 of all DIP packages: (27 Places)	
	12.6. Verify thermal sensor U20 is bonded to the PWB.	
	12.7. Verify proper clocking of transistor cans to PWB silkscreen, JFET1, JFET2	
	12.8. Verify proper installation of diodes ZD1, ZD2.	
	12.9. Verify proper orientation of pin 1 of all SIL resistor packs (RPS1, RPS2)	

13.0 Pre-Insertion Static Electrical Tests:

Note: All handling of this PWA shall be performed using ESD control methods, as outlined in MIL-STD-1686. Unit shall be inspected at an ESD certified station. Wrist straps and/or heel grounding straps shall be used.

13.1. Power circuits isolation check

- A. Set meter to "ohms", record indicated resistance between the indicated circuit points.
- B. Note orientation of (+) and (-) leads on meter.
- C. Use gold-tipped Pomona test probes for all measurements.
- D. After leads are in contact with the PWA, wait 30 seconds for meter reading to stabilize before recording measurement.

P/F	Test/Activity	Pass Criteria	Measurement
	13.1.1. TP22 (+) to TP25 neg lead (-)	> 100 kohm	Value
	13.1.2. TP23 (+) to TP25 neg lead (-)	> 100 kohm	Value
	13.1.3. TP24 (+) to TP25 neg lead (-)	> 100 kohm	Value

14.0 In-System Testing – Flight Configuration

Note: Tests run in this section are run with the hardware in “flight” configuration: no external test equipment or cables. The tests here use only the onboard diagnostic facilities of the GSS hardware. These will be the equivalent of the on-orbit tests of this system.

15.0 This section not applicable

15.0 In-System Testing – Ground Test Configuration

Note: *Tests run in this section require the addition of test cables and external test hardware. They are used to verify the board functioning of the board in fine detail, and are only used at the time of board-level test and acceptance. These may be considered “Engineering Confidence Tests”.*

15.1. Gold System Configuration

This board-level test does not require any of the services of the aft GSS unit or many of the services of the forward GSS unit. The gold system will be partially disassembled to facilitate this test.

P/F	Test/Activity	Notes
	15.1.1. Remove all gold system function cards from the forward GSS enclosure; return them to their protective ESD packaging.	
	15.1.2. Install Gold System backplane and PC610/620 test cards into a bolt-up frame GSS forward enclosure. Remove side panel to expose the component side of the DUT when installed.	
	15.1.3. Connect backplane pigtail to power supply.	
	15.1.4. Install DUT into its proper slot in the forward enclosure per P0663. (<i>note: no FSU covers are needed for this test</i>)	

15.2. Configure PC620:

P/F	Test/Activity	Notes
	15.2.1. Connect <i>BNC-to-pomona clip cable</i> to frequency generator.	
	15.2.2. Connect red (+) clip to PC620 TP84 (<i>FSU_CK34K1_A</i>)	
	15.2.3. Connect black (-) clip to PC620 AGND TP	

15.3. Gain/Phase meter setup

P/F	Test/Activity	Notes
	15.3.1. CH A: 2mV-20V setting	
	15.3.2. FREQ RANGE: 10 – 10 kHz	
	15.3.3. AMPLITUDE FNC: A	
	15.3.4. PHASE REF: A	
	15.3.5. CH B: 2mV-20V setting	

15.4. Function generator setup

P/F	Test/Activity	Notes
	15.4.1. Freq: 34.100 kHz	
	15.4.2. Amplitude: 1.0 Vpp	
	15.4.3. Waveform: SIN	

15.5. Power on:

P/F	Test/Activity	Notes
	15.5.1. Apply power to the FSU enclosure.	

15.6. Oscillator Tuning:

P/F	Test/Activity	Notes
	15.6.1. Disconnect frequency generator from PC620	
	15.6.2. Install a 200 kohm tuning on header H3.	
	15.6.3. Install a 100 kohm tuning on header H2.	
	15.6.4. Adjust pot on H3 so that TP7 reads $-3.75 \text{ V} \pm 0.05 \text{ V}$	
	15.6.5. Adjust pot on H2 so that the frequency at TP1 is $34.1 \text{ kHz} \pm 100 \text{ Hz}$. (<i>This is done via visual inspection of the signal generator and oscillator waveforms on scope screen</i>)	
	15.6.6. Reconnect frequency generator to PC620	
	15.6.7. Verify signal at TP1 is phase-locked to the input using the scope.	
	15.6.8. Verify that the signal at TP1 remains locked for frequencies $\pm 50 \text{ Hz}$ from 34,100 Hz. Adjust the pot on H2 as required to make it so.	
	15.6.9. Remove the pot on H3; record its value; return to the circuit.	Value:
	15.6.10. Subtract 100 kohm from the value on the previous line and record it here for R74.	Value: (R28)
	15.6.11. Remove the pot on H2; record its value; return to the circuit.	Value: (R74)

15.7. Phase Shifter Tuning:

P/F	Test/Activity	Notes
	15.7.1. Install a 100 kohm tuning on header H8.	
	15.7.2. Install a 100 kohm tuning on header H4.	
	15.7.3. Install a 100 kohm tuning on header H7.	
	15.7.4. Install a 100 kohm tuning on header H10.	
	15.7.5. Install a 100 kohm tuning on header H11	
	15.7.6. Adjust pot on H4 so that X_OSC reads 354 ± 0.005 mV RMS. (1 V p-p)	
	15.7.7. Adjust pot on H7 so that Y_OSC reads 354 ± 0.005 mV RMS. (1 V p-p)	
	15.7.8. Using the gain/phase meter, adjust pot on H8 so phase difference between X_OSC (Ch A) and Y_OSC (Ch B) is -120.0 ± 0.5 degrees.	
	15.7.9. Readjust pot on H7 so that X_OSC reads 354 ± 0.005 mV RMS. (1 V p-p)	
	15.7.10. Adjust pot on H10 so that Z_OSC reads 354 ± 0.005 mV RMS. (1 V p-p)	
	15.7.11. Using the gain/phase meter, adjust pot on H8 so phase difference between X_OSC (Ch A) and Z_OSC (Ch B) is +120.0 ± 0.5 degrees.	
	15.7.12. Readjust pot on H10 so that Z_OSC reads 354 ± 0.005 mV RMS. (1 V p-p)	
	15.7.13. Remove the pot on H4; record value:	Value: (R2+R6)
	15.7.14. Remove the pot on H7; record value:	Value: (R20+R24)
	15.7.15. Remove the pot on H8; record value:	Value: (R26+R30)
	15.7.16. Remove the pot on H10; record value:	Value: (R36+R42)
	15.7.17. Remove the pot on H11; record value:	Value: (R46+R47)

15.8. Record tuning values

P/F	Test/Activity	Notes
	15.8.1. Use the Single (Section 17.0) and Dual (Section 18.0) resistor selection tables to choose the appropriate fixed value resistors for the components below. <i>Note, the dual-resistor tables are for series wired resistor values only.</i> (All Rx+Ry types)	

Component	Flight PN	Description	Value transferred from earlier in the procedure
R2	RNC55H_____FS		(R2+R6)
R6	RNC55H_____FS		
R26	RNC55H_____FS		(R26+R30)
R30	RNC55H_____FS		
R20	RNC55H_____FS		(R20+R24)
R24	RNC55H_____FS		
R36	RNC55H_____FS		(R36+R42)
R42	RNC55H_____FS		
R28	RNC55H_____FS		(R28)
R74	RNC55H_____FS		(R74)
R41	open	no component installed	no component installed
R46	RNC55H_____FS		(R46+R47)
R47	RNC55H_____FS		

	15.8.2. Transfer these values to the select and test installation sheet in Appendix A.
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16.0 Completion of Procedure:

	P/F	Notes
16.1. Remove all tuning pots from PWA.		
16.2. Remove PWA from enclosure per P0663 and return to storage container.		
16.3. Return the board to the assembly vendor for installation of the final values of the selected resistors.		

The results obtained in the performance of this test procedure are acceptable.

Test Engineer

Date

This is to certify that the information obtained under this test procedure is as represented and the documentation is completed and correct.

GSS
Representative

Date

Quality
Assurance

Date

17.0 Single Resistor Value Selection Table

For single select and test resistors, find the row that brackets the value of interest between Min and Max; use the specified resistor given in the description and P/N columns. (all values are in ohms)

For values in this range		Choose this resistor:	
Min (ohms)	Max (Ohms)	Description	P/N
1000	1134	Resistor, 1.00K Ohms, ±1%	RN55H1001FS
1034	1109	Resistor, 1.07K Ohms, ±1%	RN55H1071FS
1109	1194	Resistor, 1.15K Ohms, ±1%	RN55H1151FS
1194	1284	Resistor, 1.24K Ohms, ±1%	RN55H1241FS
1284	1379	Resistor, 1.33K Ohms, ±1%	RN55H1331FS
1379	1484	Resistor, 1.43K Ohms, ±1%	RN55H1431FS
1484	1594	Resistor, 1.54K Ohms, ±1%	RN55H1541FS
1594	1714	Resistor, 1.65K Ohms, ±1%	RN55H1651FS
1714	1844	Resistor, 1.78K Ohms, ±1%	RN55H1781FS
1844	1979	Resistor, 1.91K Ohms, ±1%	RN55H1911FS
1979	2128	Resistor, 2.05K Ohms, ±1%	RN55H2051FS
2128	2289	Resistor, 2.21K Ohms, ±1%	RN55H2211FS
2289	2458	Resistor, 2.37K Ohms, ±1%	RN55H2371FS
2458	2643	Resistor, 2.55K Ohms, ±1%	RN55H2551FS
2643	2838	Resistor, 2.74K Ohms, ±1%	RN55H2741FS
2838	3048	Resistor, 2.94K Ohms, ±1%	RN55H2941FS
3048	3278	Resistor, 3.16K Ohms, ±1%	RN55H3161FS
3278	3523	Resistor, 3.40K Ohms, ±1%	RN55H3401FS
3523	3783	Resistor, 3.65K Ohms, ±1%	RN55H3651FS
3783	4067	Resistor, 3.92K Ohms, ±1%	RN55H3921FS
4067	4372	Resistor, 4.22K Ohms, ±1%	RN55H4221FS
4372	4697	Resistor, 4.53K Ohms, ±1%	RN55H4531FS
4697	4930	Resistor, 4.87K Ohms, ±1%	RN55H4871FS
4930	5109	Resistor, 4.99K Ohms, ±1%	RN55H4991FS
5109	5421	Resistor, 5.23K Ohms, ±1%	RN55H5231FS
5421	5826	Resistor, 5.62K Ohms, ±1%	RN55H5621FS
5826	6261	Resistor, 6.04K Ohms, ±1%	RN55H6041FS
6261	6731	Resistor, 6.49K Ohms, ±1%	RN55H6491FS
6731	7235	Resistor, 6.98K Ohms, ±1%	RN55H6981FS
7235	7775	Resistor, 7.50K Ohms, ±1%	RN55H7501FS
7775	8355	Resistor, 8.06K Ohms, ±1%	RN55H8061FS
8355	8979	Resistor, 8.66K Ohms, ±1%	RN55H8661FS
8979	9649	Resistor, 9.31K Ohms, ±1%	RN55H9311FS

Single Resistor Value Selection Table (continued)

For values in this range		Choose this resistor:	
Min (ohms)	Max (Ohms)	Description	P/N
9649	10368	Resistor, 10.0K Ohms, ±1%	RN55H1002FS
10368	11143	Resistor, 10.7K Ohms, ±1%	RN55H1072FS
11143	11972	Resistor, 11.5K Ohms, ±1%	RN55H1152FS
11972	12867	Resistor, 12.4K Ohms, ±1%	RN55H1242FS
12867	13826	Resistor, 13.3K Ohms, ±1%	RN55H1332FS
13826	14855	Resistor, 14.3K Ohms, ±1%	RN55H1432FS
14855	15965	Resistor, 15.4K Ohms, ±1%	RN55H1542FS
15965	17154	Resistor, 16.5K Ohms, ±1%	RN55H1652FS
17154	18433	Resistor, 17.8K Ohms, ±1%	RN55H1782FS
18433	19812	Resistor, 19.1K Ohms, ±1%	RN55H1912FS
19812	21291	Resistor, 20.5K Ohms, ±1%	RN55H2052FS
21291	22875	Resistor, 22.1K Ohms, ±1%	RN55H2212FS
22875	24579	Resistor, 23.7K Ohms, ±1%	RN55H2372FS
24579	26413	Resistor, 25.5K Ohms, ±1%	RN55H2552FS
26413	28387	Resistor, 27.4K Ohms, ±1%	RN55H2742FS
28387	30505	Resistor, 29.4K Ohms, ±1%	RN55H2942FS
30505	32779	Resistor, 31.6K Ohms, ±1%	RN55H3162FS
32779	35227	Resistor, 34.0K Ohms, ±1%	RN55H3402FS
35227	37856	Resistor, 36.5K Ohms, ±1%	RN55H3652FS
37856	40679	Resistor, 39.2K Ohms, ±1%	RN55H3922FS
40679	43717	Resistor, 42.2K Ohms, ±1%	RN55H4222FS
43717	46980	Resistor, 45.3K Ohms, ±1%	RN55H4532FS
46980	49296	Resistor, 48.7K Ohms, ±1%	RN55H4872FS
49296	51101	Resistor, 49.9K Ohms, ±1%	RN55H4992FS
51101	54245	Resistor, 52.3K Ohms, ±1%	RN55H5232FS
54245	58292	Resistor, 56.2K Ohms, ±1%	RN55H5622FS
58292	62644	Resistor, 60.4K Ohms, ±1%	RN55H6042FS
62644	67317	Resistor, 64.9K Ohms, ±1%	RN55H6492FS
67317	72338	Resistor, 69.8K Ohms, ±1%	RN55H6982FS
72338	77735	Resistor, 75.0K Ohms, ±1%	RN55H7502FS
77735	83536	Resistor, 80.6K Ohms, ±1%	RN55H8062FS
83536	89772	Resistor, 86.6K Ohms, ±1%	RN55H8662FS
89772	96468	Resistor, 93.1K Ohms, ±1%	RN55H9312FS
96468	100000	Resistor, 100K Ohms, ±1%	RN55H1003FS

Board-level tuning procedure P0611A
 GSS MUX card, PWA 8A01883

128,098	129,050	128,800	42200	+	86600
129,050	129,350	129,300	48700	+	80600
129,350	129,500	129,400	29400	+	100000
129,500	129,700	129,600	36500	+	93100
129,700	130,000	129,800	64900	+	64900
130,000	130,699	130,200	60400	+	69800
130,699	131,400	131,200	56200	+	75000
131,400	131,750	131,600	31600	+	100000
131,750	132,100	131,900	45300	+	86600
132,100	132,600	132,300	39200	+	93100
132,600	133,449	132,900	52300	+	80600
133,449	134,350	134,000	34000	+	100000
134,350	135,000	134,700	64900	+	69800
135,000	135,350	135,300	42200	+	93100
135,350	135,949	135,400	60400	+	75000
135,949	136,650	136,500	36500	+	100000
136,650	137,598	136,800	56200	+	80600
137,598	138,650	138,400	45300	+	93100
138,650	139,050	138,900	52300	+	86600
139,050	139,400	139,200	39200	+	100000
139,400	139,750	139,600	69800	+	69800
139,750	140,449	139,900	64900	+	75000
140,449	141,399	141,000	60400	+	80600
141,399	142,000	141,800	48700	+	93100
142,000	142,500	142,200	42200	+	100000
142,500	143,797	142,800	56200	+	86600
143,797	145,050	144,800	69800	+	75000
145,050	145,350	145,300	45300	+	100000
145,350	145,450	145,400	52300	+	93100
145,450	146,248	145,500	64900	+	80600
146,248	147,848	147,000	60400	+	86600
147,848	149,000	148,700	48700	+	100000
149,000	149,650	149,300	56200	+	93100
149,650	150,200	150,000	75000	+	75000
150,200	150,949	150,400	69800	+	80600
150,949	151,899	151,500	64900	+	86600
151,899	152,899	152,300	52300	+	100000
152,899	154,546	153,500	60400	+	93100
154,546	155,900	155,600	75000	+	80600
155,900	156,300	156,200	56200	+	100000
156,300	157,198	156,400	69800	+	86600
157,198	159,195	158,000	64900	+	93100
159,195	160,800	160,400	60400	+	100000
160,800	161,400	161,200	80600	+	80600
161,400	162,249	161,600	75000	+	86600
162,249	163,897	162,900	69800	+	93100
163,897	166,046	164,900	64900	+	100000
166,046	167,649	167,200	80600	+	86600
167,649	168,948	168,100	75000	+	93100

168,948	171,492	169,800	69800	+	100000
171,492	173,450	173,200	86600	+	86600
173,450	174,349	173,700	80600	+	93100
174,349	177,334	175,000	75000	+	100000
177,334	180,149	179,700	86600	+	93100
180,149	183,379	180,600	80600	+	100000
183,379	186,400	186,200	93100	+	93100
186,400	189,822	186,600	86600	+	100000
189,822	196,520	193,100	93100	+	100000
196,520	200,000	200,000	100000	+	100000



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Gravity Probe B Relativity Mission

P0611A APPENDIX A
SELECT AND TEST COMPONENT INSTALLATION
INSTRUCTION SHEET: GSS MUX PWA

PWA 8A01883 Rev C S/N:

From the findings of procedure P0611 “BOARD-LEVEL TUNING PROCEDURE FOR THE GYROSCOPE SUSPENSION SYSTEM (GSS) MUX/OSCILLATOR/CHARGE CTRL (MUX) BOARD”, install the following select-and-test components as indicated below:

★NOTE: See 8A01883 Rev C note 27 for installation of R26, R30, R46, R47.

Component	Flight PN	Description	Mfgr	LDC
R2	RNC55H_____FS			
R6	RNC55H_____FS			
R26 ★	RNC55H_____FS			
R30 ★	RNC55H_____FS			
R20	RNC55H_____FS			
R24	RNC55H_____FS			
R36	RNC55H_____FS			
R42	RNC55H_____FS			
R28	RNC55H_____FS			
R74	RNC55H_____FS			

(continued)

R41	open	no component installed		
R46 ★	RNC55H_____FS			
R47 ★	RNC55H_____FS			

Approved:

GSS Representative	<input type="text"/>	Date	<input type="text"/>
Quality Assurance	<input type="text"/>	Date	<input type="text"/>