



W. W. Hansen Experimental Physics Laboratory  
STANFORD UNIVERSITY  
STANFORD, CALIFORNIA 94305-4085

Gravity Probe B Relativity Mission

## BOARD-LEVEL TEST PROCEDURE FOR THE GYROSCOPE SUSPENSION SYSTEM (GSS) ACU AFT TEST CARD (ATC) BOARD

PWA 8A01900 Rev A S/N:

### GP-B Procedure P0600 Rev A

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Prepared by: Scott Smader  
RE, ATC

Date

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Approved by: William Bencze  
RE, Gyroscope Suspension System (GSS) Group

Date

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Approved by: Dorrene Ross  
GP-B Quality Assurance

Date

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**1.0 Revision History**

Rev Level	Comments/notes	Date	Revised By
-	First release of this test procedure	29-Feb-00	S Smader
A0	Clarify usage of AMT software	5-Apr-00	S Smader
A1	Typos	6-Apr-00	S Smader
A	Second release	7-Apr-00	S Smader

## 2.0 Scope:

This procedure details the board-level electrical functional tests on the GSS Aft Test Card (ATC) card. No mechanical or thermal stress testing shall be performed at this time.

This test plan has been written to be run with the GSS "Gold System" test fixture – an electrically and interface equivalent of the GSS flight units. In General, the Device Under Test (DUT) shall be inserted into the Gold System in place of the equivalent Gold System card, any additional electrical connections to the Gold System shall be made, and a set of software-based and possibly manual tests will be run on the board. Upon successful completion of this procedure, this board is considered electrically functional.

All data recorded during this test is recorded in this document; each test of a board will use its own copy of this procedure, and will be identified by serial number in the upper right corner.

## 3.0 Reference Documents

- 3.1. GSS Gold System Hardware and Software Configuration Standard, P0663
- 3.2. PWA Drawing, GSS Aft Test Card board, 8A01900
- 3.3. PWB Drawing, GSS Aft Test Card board, 8A01875
- 3.4. Board-level Test Procedure for the Gyroscope Suspension System (GSS) Aft Monitor and Timing (AMT) Board, P0597
- 3.5. Electrostatic Discharge Control Program for Protection of Electrical and Electronic Parts, Assemblies, and Equipment, MIL-STD-1686

## 4.0 Test Facilities

HEPL Room 127, Stanford University

## 5.0 QA Provisions:

- 5.1. This procedure shall be conducted on a formal basis to its latest approved and released version. The QA Program Engineer (D. Ross) and the ONR representative (E. Ingraham) shall be notified 24 hours prior to the start of this procedure. QA may monitor the execution of all or part of this procedure should they elect to do so.

Date/time: \_\_\_\_\_  
GP-B QA (D. Ross)

Date/time: \_\_\_\_\_  
ONR (E. Ingraham)

- 5.2. Upon completion of this procedure, the GSS manager and the GP-B QA manager shall certify her/his concurrence that the procedure was performed and accomplished in accordance with the prescribed instructions by signing and dating his approval at the end of this procedure.

Board S/N:

**6.0 Test Personnel**

This test procedure is to be conducted only by the following certified personnel:

- 6.1. William Bencze
- 6.2. Scott Smader
- 6.3. Joe Kilner
- 6.4. Lo Van Ho

**7.0 General Instructions**

- 7.1. Redlines can be initiated by the certified test personnel listed in Section 6.0 and must be approved by QA.
- 7.2. Test operators shall read this procedure in its entirety and resolve any apparent ambiguities prior to beginning this test.
- 7.3. Any nonconformance or test anomaly should be reported by a Discrepancy Report. Refer to the Quality Plan, P0108, for guidance. Do not alter or break test configuration if a test failure occurs; notify quality assurance.
- 7.4. Only the following persons have the authority to exit/terminate this test or perform a retest: Certified test operators listed in Section 6.0 and GP-B QA.

**8.0 Hardware Safety Requirements:**

- 8.1. This assembly is ESD sensitive; special care shall be exercised per the "Electrostatic Discharge Control Program for Protection of Electrical and Electronic Parts, Assemblies, and Equipment", MIL-STD-1686
- 8.2. Ensure that power is removed from cable assemblies before connecting or disconnecting cable connectors.
- 8.3. Examine all mating connectors before attempting to mate them. Remove any foreign particles. Look for any damaged pins or sockets. Do not force the coupling action if excessive resistance is encountered. Ensure that key-ways are aligned when mating connectors.

**9.0 Equipment Pretest Requirements:**

- 9.1. The GSS Gold System in which this board is to be tested must have passed successfully the P0663 – Gold System Certification Procedure prior to the start of this test. Record the Gold System serial number and date of its certification, below.

GSS Gold System	SN:	
	Date of Certification	
	Configuration (circle one)	Full    Partial

**10.0 Additional Test Equipment**

The following support hardware, test equipment, or software will be used and the applicable information for the instruments shall be recorded below. Hand-written additions to this may be added in the space provided.

Equipment Description	Make	Model	SN	Cal Due
1. Multimeter	Fluke			
2.				
3.				
4.				
5.				
6.				
7.				
8.				
9.				
10.				

**11.0 Device Under Test (DUT):**

Record the serial number of the Device Under Test, or DUT.

PWA 8A01900A GSS Aft Test Card	SN:	
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Test Operator:	Name:	
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Start of test:	Date:	
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**12.0 Pre-test visual inspection.**

*Note: All handling of this PWA shall be performed using ESD control methods, as outlined in MIL-STD-1686. Unit shall be inspected at an ESD certified station. Wrist straps and/or heel grounding straps shall be used.*

	P/F	Notes
12.1. Remove PWA from storage container.		
12.2. Verify that no parts are missing, unless called out in the assembly drawing.		
12.3. Verify the proper orientation of pin 1 of all DIP packages: U2.		

**13.0 Pre-Insertion Static Electrical Tests:**

*Note: All handling of this PWA shall be performed using ESD control methods, as outlined in MIL-STD-1686. Unit shall be inspected at an ESD certified station. Wrist straps and/or heel grounding straps shall be used.*

13.1. Power circuits isolation check

Set meter to "ohms"; record resistance between the indicated pins.

Test	Notes	Allowed	P/F	Measurement
13.1.1. P1-120 to P1-118	P5V to DGND	> 100 Kohms		
13.1.2. P1-120 to P1-1	P5V to P15V	> 100 Kohms		
13.1.3. P1-1 to P1-118	P15V to DGND	> 100 Kohms		
13.1.4. P1-1 to P1-2	P15V to AGND	> 100 Kohms		
13.1.5. P1-2 to P1-120	AGND to P5V	> 100 Kohms		

*Note: AGND and DGND should be shorted by the external power supply, but this is not part of the DUT qualification process.*

DUT passes this section if all measurements are greater than indicated condition.

#### 14.0 In-System Testing – Flight Configuration

*Note: Tests run in this section are run with the hardware in “flight” configuration: no external test equipment or cables. The tests here use only the onboard diagnostic facilities of the GSS hardware. These will be the equivalent of the on-orbit tests of this system.*

**This section not applicable.**



**15.0 In-System Testing – Ground Test Configuration**

*Note: Tests run in this section require the addition of test cables and external test hardware. They are used to verify the board functioning of the board in fine detail, and are only used at the time of board-level test and acceptance. These may be considered “Engineering Confidence Tests”.*

15.1. Board installation:

	P/F	Notes
15.1.1. Install PWA in gold system enclosure per the instructions in P0663 – Gold System certification procedure		
15.1.2. Cable system for “Configuration A” per P0663 – Gold System certification procedure		

With power applied for at least 5 minutes to allow ambient temperature to stabilize and thermal sensors to reach ambient temperature, run AMT Diagnostic test “T1.exe” as described in P0597, sections 15.5 and 15.6. Record and calculate the following information.

15.2. Board Tests:

	Value
15.2.1. Run AMT Diagnostic. Record ATC temperature (“ATC Temp Mon”).	(hex)
15.2.2. Record AMT temperature (“AMT Temp Mon”).	(hex)
15.2.3. Subtract the smaller of 13.2.1 or 13.2.2 from the larger; record result here:	(hex)

	P/F	Notes
15.2.4. Device passes if difference is < 0x0A0		

*Note: A difference of 0xA0 A/D counts is equivalent to an approximately 5 degree Celsius difference in indicated temperatures.*

**16.0 Completion of Procedure:**

	P/F	Notes
16.1. Turn off power to FSU and ACU enclosures.		
16.2. Remove PWA from enclosure per P0663 and return to storage container.		

I certify that this procedure was performed in whole and that the data recorded above is complete and accurate.

Test Engineer  Date

This is to certify that the information obtained under this test procedure is as represented and the documentation is completed and correct.

GSS Representative  Date

Quality Assurance  Date