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Gravity Probe B Relativity Mission

BOARD-LEVEL TEST PROCEDURE FOR THE GYROSCOPE SUSPENSION SYSTEM (GSS) ACU AFT COMMUNICATIONS LINK (ACL) BOARD

PWA 8A01897 Rev A S/N:

GP-B Procedure P0599 Rev A

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RE, ACL

Date

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Date

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GP-B Quality Assurance

Date

Table of Contents:

1.0 Revision History _____ 2
 2.0 Scope: _____ 3
 3.0 Reference Documents _____ 3
 4.0 Test Facilities _____ 3
 5.0 QA Provisions: _____ 3
 6.0 Test Personnel _____ 4
 7.0 General Instructions _____ 4
 8.0 Hardware Safety Requirements: _____ 4
 9.0 Equipment Pretest Requirements: _____ 5
 10.0 Additional Test Equipment _____ 5
 11.0 Device Under Test (DUT): _____ 5
 12.0 Pre-test visual inspection. _____ 6
 13.0 Pre-Insertion Static Electrical Tests: _____ 7
 14.0 In-System Testing – Flight Configuration _____ 8
 15.0 In-System Testing – Ground Test Configuration _____ 10
 16.0 Completion of Procedure: _____ 11

1.0 Revision History

| Rev Level | Comments/notes | Date | Revised By |
|-----------|--|-----------|------------|
| - | First release of this test procedure | 29-Feb-00 | S Smader |
| A0 | Replace ADDA procedure by Flt Cmd 16,1,0x20E | 3-Mar-00 | S Smader |
| A1 | Clarify software command usage | 4-Apr-00 | S Smader |
| A2 | Typos | 6-Apr-00 | S Smader |
| A | Second release | 7-Apr-00 | S Smader |

2.0 Scope:

This procedure details the board-level electrical functional tests on the GSS Aft Comm Link (ACL) card. No mechanical or thermal stress testing shall be performed at this time.

This test plan has been written to be run with the GSS "Gold System" test fixture – an electrically and interface equivalent of the GSS flight units. In General, the Device Under Test (DUT) shall be inserted into the Gold System in place of the equivalent Gold System card, any additional electrical connections to the Gold System shall be made, and a set of software-based and possibly manual tests will be run on the board. Upon successful completion of this procedure, this board is considered electrically functional.

All data recorded during this test is recorded in this document; each test of a board will use its own copy of this procedure, and will be identified by serial number in the upper right corner.

3.0 Reference Documents

- 3.1. GSS Gold System Hardware and Software Configuration Standard, P0663
- 3.2. PWA Drawing, GSS Aft Comm Link board, 8A01897
- 3.3. PWB Drawing, GSS Aft Comm Link board, 8A01871
- 3.4. Board-level test software operational procedure, P0670
- 3.5. Electrostatic Discharge Control Program for Protection of Electrical and Electronic Parts, Assemblies, and Equipment, MIL-STD-1686

4.0 Test Facilities

HEPL Room 127, Stanford University.

5.0 QA Provisions:

- 5.1. This procedure shall be conducted on a formal basis to its latest approved and released version. The QA Program Engineer (D. Ross) and the ONR representative (E. Ingraham) shall be notified 24 hours prior to the start of this procedure. QA may monitor the execution of all or part of this procedure should they elect to do so.

Date/time: _____
GP-B QA (D. Ross)

Date/time: _____
ONR (E. Ingraham)

- 5.2. Upon completion of this procedure, the GSS manager and the GP-B QA manager shall certify her/his concurrence that the procedure was performed and accomplished in accordance with the prescribed instructions by signing and dating his approval at the end of this procedure.

| |
|--|
| |
|--|

6.0 Test Personnel

This test procedure is to be conducted only by the following certified personnel:

- 6.1. William Bencze
- 6.2. Scott Smader
- 6.3. Joe Kilner
- 6.4. Lo Van Ho

7.0 General Instructions

- 7.1. Redlines can be initiated by the certified test personnel listed in Section 6.0 and must be approved by QA.
- 7.2. Test operators shall read this procedure in its entirety and resolve any apparent ambiguities prior to beginning this test.
- 7.3. Any nonconformance or test anomaly should be reported by a Discrepancy Report. Refer to the Quality Plan, P0108, for guidance. Do not alter or break test configuration if a test failure occurs; notify quality assurance.
- 7.4. Only the following persons have the authority to exit/terminate this test or perform a retest: Certified test operators listed in Section 6.0 and GP-B QA.
- 7.5. In this document, "Perform Flight S/W system test commands:" means to prepare the test system software as described in P0670 Board-Level Test Software Operational Procedure, and then issue the listed commands according to the procedure described in P0670.

8.0 Hardware Safety Requirements:

- 8.1. This assembly is ESD sensitive; special care shall be exercised per the "Electrostatic Discharge Control Program for Protection of Electrical and Electronic Parts, Assemblies, and Equipment", MIL-STD-1686
- 8.2. Ensure that power is removed from cable assemblies before connecting or disconnecting cable connectors.
- 8.3. Examine all mating connectors before attempting to mate them. Remove any foreign particles. Look for any damaged pins or sockets. Do not force the coupling action if excessive resistance is encountered. Ensure that key-ways are aligned when mating connectors.

Board S/N:

9.0 Equipment Pretest Requirements:

- 9.1. The GSS Gold System in which this board is to be tested must have passed successfully the P0663 – Gold System Certification Procedure prior to the start of this test. Record the Gold System serial number and date of its certification, below

| | | |
|-----------------|-------------------------------|-----------------|
| GSS Gold System | SN: | |
| | Date of Certification | |
| | Configuration (circle one) | Full Partial |

10.0 Additional Test Equipment

The following support hardware, test equipment, or software will be used and the applicable information for the instruments shall be recorded below. Hand-written additions to this list as needed.

| Equipment Description | Make | Model | SN | Cal Due |
|-----------------------|-------|-------|----|---------|
| 1. Multimeter | Fluke | | | |
| 2. | | | | |
| 3. | | | | |
| 4. | | | | |

11.0 Device Under Test (DUT):

Record the serial number of the Device Under Test, or DUT.

| | | |
|--------------------------------|-----|--|
| PWA 8A01901A GSS Aft Comm Link | SN: | |
|--------------------------------|-----|--|

| | | |
|----------------|-------|--|
| Test Operator: | Name: | |
|----------------|-------|--|

| | | |
|----------------|-------|--|
| Start of test: | Date: | |
|----------------|-------|--|

12.0 Pre-test visual inspection.

Note: All handling of this PWA shall be performed using ESD control methods, as outlined in MIL-STD-1686. Unit shall be inspected at an ESD certified station. Wrist straps and/or heel grounding straps shall be used.

| | P/F | Notes |
|--|-----|-------|
| 12.1. Remove PWA from storage container. | | |
| 12.2. Verify that no parts are missing, unless called out in the assembly drawing. | | |
| 12.3. Verify that the following capacitors are installed in the proper orientation: C52. | | |
| 12.4. Verify the proper orientation of pin 1 of all DIP packages: U1 through U51. | | |

13.0 Pre-Insertion Static Electrical Tests:

Note: All handling of this PWA shall be performed using ESD control methods, as outlined in MIL-STD-1686. Unit shall be inspected at an ESD certified station. Wrist straps and/or heel grounding straps shall be used.

13.1. Power circuits isolation check

Set meter to "ohms", record indicated resistance between the indicated pins.

| | P/F | Notes |
|------------------------|-----|-------|
| 13.1.1. J1-58 to J1-60 | | |

DUT passes this section if 11.1.1 is greater than 100 Kohm.

14.0 In-System Testing – Flight Configuration

Note: Tests run in this section are run with the hardware in “flight” configuration: no external test equipment or cables. The tests here use only the onboard diagnostic facilities of the cGSS hardware. These will be the equivalent of the on-orbit tests of this system.

Except at the discretion of the test director, these tests shall not be run before all Pre-Insertion Static Electrical Tests and all In-System Ground Configuration Tests have successfully completed.

14.1. Board installation:

| | P/F | Notes |
|--|-----|-------|
| 14.1.1. Install PWA in gold system enclosure per the instructions in P0663 – Gold System certification procedure | | |
| 14.1.2. Cable system for “Configuration B” per P0663 – Gold System certification procedure | | |

Record the Pass/Fail return status of each of the following commands.

14.2. System Functional Tests:

| | P/F |
|--|-----|
| 14.2.1. Perform Flight S/W system test commands: 16, 100 16, 2, 0x0101, 20 1, 1 16, 2, 0x0100, 7 16, 1, 0x020E If the value of "PIT 3-diag. Monitors", "Monitor 3:" is 0x0003, record 'Pass' for this test. Otherwise record 'Fail'. | |
| 14.2.2. Perform Flight S/W system test commands: 14, 1 14, 38 If this completes successfully, record Pass for this test. Otherwise record 'Fail'. | |
| 14.2.3. Perform Flight S/W system test commands: 14, 1 14, 29 If this completes successfully, record Pass for this test. Otherwise record 'Fail'. | |

DUT passes this section if all of the return values are Pass.

15.0 In-System Testing – Ground Test Configuration

Note: Tests run in this section require the addition of test cables and external test hardware. They are used to verify the board functioning of the board in fine detail, and are only used at the time of board-level test and acceptance. These may be considered "Engineering Confidence Tests".

This section not applicable.

Board S/N:

16.0 Completion of Procedure:

| | P/F |
|--|-----|
| 16.1. Turn off power to FSU and ACU enclosures. | |
| 16.2. Remove PWA from enclosure per P0663 and return to storage container. | |

Notes

I certify that the this procedure was performed in whole and that the data recorded above is complete and accurate.

Test Engineer Date

This is to certify that the information obtained under this test procedure is as represented and the documentation is completed and correct.

GSS Representative Date

Quality Assurance Date